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10/765,554	01/27/2004	David L. McClintock	016295.1517	1645

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Attn: Bradley S. Bowling  
Baker Botts L.L.P.  
910 Louisiana Street  
Houston, TX 77002-4995

EXAMINER
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CRAWFORD, JACINTA M

ART UNIT	PAPER NUMBER
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2628

MAIL DATE	DELIVERY MODE
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10/17/2007

PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

## Office Action Summary

Application No.

10/765,554

Applicant(s)

MCCLINTOCK ET AL.

Examiner

Jacinta Crawford

Art Unit

2628

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 06 August 2007.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |  |   |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                       | 5) <input type="checkbox"/> Notice of Informal Patent Application                       |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

**DETAILED ACTION**

***Claim Rejections - 35 USC § 103***

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 1-14, 16-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lafleur (US 7,123,248) in view of Trottier et al. (US 6,903,706).

As to claim 1, Lafleur disclose a video display controller (Figure 3, element 2), comprising:

a graphics processing unit (Figure 3, element 4, video processor) adapted to receive input and transmit output to one or more display devices (column 3, lines 50-60); and

a single display device connector (Figure 3, element 6, DVI-I connector) in communication with the graphics processing unit (Figure 3, elements 12a and 14a notes communication);

wherein the video display controller is adapted to control a first display device (Figure 3, element 16a) and a second display device (Figure 3, element 16b) through the display device connector (Figure 3, element 6, DVI-I).

Lafleur differs from the invention defined in claim 1 in that Lafleur does not disclose the video controller independently controlling a first display and a second display.

Trottier et al. disclose a video controller independently controlling a first display and a second display (abstract).

It would have been obvious to one of ordinary skill in the art at the time of the invention to modify Lafleur's video display controller with Trottier et al.'s method of controlling displays independently to provide more features and options for displays to be used for various multi-purpose tasks.

As to claim 2, Lafleur discloses the video display controller (Figure 3, element 2) where the display device connector is a DVI-I connector (Figure 3, element 6; column 3, lines 46-47).

As to claim 3, Lafleur discloses the video display controller where the first display device (Figure 3, element 16a) is an analog display device (Figure 3,

Art Unit: 2628

element 14a).

As to claim 4, Lafleur discloses the video display controller where the second display device (Figure 3, element 16b) is a digital display device (element 12a).

As to claim 5, Lafleur discloses the video display controller comprising a first control channel and a second control channel (Figure 4).

As to claim 6, Lafleur discloses the video display controller where the first control channel is adapted to use analog.

Lafleur discloses remapping the TMDS signals and replacing them with analog signals for dual display outputs (see Figure 4 and column 4, lines 16-28).

Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate this same method using Display Data Channel Command Interface.

As to claim 7, Lafleur discloses the video display controller where the second control channel is adapted to use analog.

Art Unit: 2628

Lafleur discloses remapping the TMDS signals and replacing them with analog signals for dual display outputs (see Figure 4 and column 4, lines 16-28).

Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the channel to use Display Data Channel Command Interface.

As to claim 8, Lafleur discloses a dongle (Figure 3, element 18; column 3, lines 48-49), for connecting a video display controller (Figure 3, element 2) with a first display device (Figure 3, element 16a) and a second display device (Figure 3, element 16b), the video display controller comprising a graphics processing unit (Figure 3, element 4, video processor) adapted to receive input and transmit output to one or more display devices (column 3, lines 50-60), the video display controller further comprising a single display device connector (Figure 3, element 6, DVI-I connector) in communication with the graphics processing unit (Figure 3, elements 12a and 14a notes communication), and wherein the video display controller is adapted to control the first display device (Figure 3, element 16a) and the second display device (Figure 3, element 16b) through the display device connector (Figure 3, element 6, DVI-I connector), the dongle comprising:

routing circuitry capable of:

routing a first video channel and a first control channel from the video display

Art Unit: 2628

controller to the first display device (Figure 3, elements 4 to 6 to 16a), and routing a second video channel and a second control channel from the video display controller to the second display device (Figure 3, elements 4 to 6 to 16b)(NOTE: arrows routing the channels to the appropriate display device).

Lafleur differs from the invention defined in claim 8 in that Lafleur does not disclose the video controller independently controlling a first display and a second display.

Trottier et al. disclose a video controller independently controlling a first display and a second display (abstract).

It would have been obvious to one of ordinary skill in the art at the time of the invention to modify Lafleur's video display controller with Trottier et al.'s method of controlling displays independently to provide more features and options for displays to be used for various multi-purpose tasks.

As to claim 9, Lafleur discloses the dongle where the first video channel is a TMDS channel (Figure 4) and the second video channel is an analog VGA channel (Figure 4).

As to claim 10, Lafleur discloses the dongle where the first video channel is a TMDS channel (Figure 4) and the second video channel is a TMDS channel (Figure 4).

As to claim 11, Lafleur discloses the dongle where the first control channel and the second control channel are adapted to use analog (Figure 4 and column 4, lines 16-28).

However, it would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate this same method using Display Data Channel Command Interface.

As to claim 12, Lafleur does not disclose the dongle comprising a dongle detection circuit, wherein the dongle detection circuit signals the video display controller that the dongle is attached to the video display controller.

However, it would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate a detection circuit to detect when the dongle is attached to the video display controller in order to control and properly implement the control channels to the appropriate display.

As to claim 13, Lafleur discloses an information handling system, comprising:



Art Unit: 2628

a first display device (Figure 3, element 16a);

a second display device (Figure 3, element 16b);

a video display controller (Figure 3, element 2) in communication with the first display device and the second display device (Figure 3: note the communication is denoted by the arrows), the video display controller comprising:

a graphics processing unit (Figure 3, element 4, video processor) adapted to receive input and transmit output to one or more display devices (column 3, lines 50-60); and

a single display device connector (Figure 3, element 6, DVI-I connector) in communication with the graphics processing unit (Figure 3, elements, 12a and 14a);

wherein the video display controller is adapted to control the first display device (Figure 3, element 16a) and the second display device (Figure 3, element 16b) through the single display device connector (Figure 3, element 6, DVI-I connector).

Lafleur differs from the invention defined in claim 13 in that Lafleur does not disclose the video controller independently controlling a first display and a second display.

Art Unit: 2628

Trottier et al. disclose a video controller independently controlling a first display and a second display (abstract).

It would have been obvious to one of ordinary skill in the art at the time of the invention to modify Lafleur's video display controller with Trottier et al.'s method of controlling displays independently to provide more features and options for displays to be used for various multi-purpose tasks.

As to claim 14, Lafleur modified with Trottier et al. do not disclose the information handling system comprising a housing, wherein the video display controller is within the housing.

However, it would have been obvious to one of ordinary skill in the art at the time of the invention to provide a housing for the video display controller to serve as a protection for the internal components of the device.

As to claim 16, Lafleur modified with Trottier et al. do not disclose the information handling system where the housing is a laptop housing.

The Examiner takes an official notice as to the laptop housing.

Art Unit: 2628

It would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate a laptop housing because laptop housing are relatively small and thin and will ultimately reduce the size of the overall device.

As to claim 17, Lafleur discloses the information handling system where the first display device (Figure 3, element 16a) is an analog display device (Figure 3, 14a).

As to claim 18, Lafleur discloses the information handling system where the second display device (Figure 3, element 16b) is a digital display device (Figure 3, element 12a).

As to claim 19, Lafleur discloses the information handling system where the second display device is capable of receiving TMDS (Figure 4).

As to claim 20, Lafleur discloses the information handling system comprising: a dongle (Figure 3, element 18; column 3, lines 48-49) comprising circuitry capable of:

routing a first video channel and a first control channel from the video display controller to the first display device (Figure 3, elements 4 to 6 to 16a), and routing a second video channel and a second control channel from the

Art Unit: 2628

video display controller to the second display device (Figure 3, elements 4 to 6 to 16b)(NOTE: arrows routing the channels to the appropriate display device).

3. Claim 15 is rejected under 35 U.S.C. 103(a) as being unpatentable over Lafleur (US 7,123,248) and Trottier et al. (US 6,903,706) as applied to claim 14 above, and further in view of Reichle (US 2005/0118880).

As to claim 15, Lafleur modified with Trottier et al. do not disclose the information handling system where the housing is a Small Form Factor (SFF) housing.

Reichle discloses the information handling system where the housing is a Small Form Factor (SFF) housing ([0045], lines 7-11).

It would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate a Small Form Factor housing to reduce the size of the housing, which will ultimately reduce the overall size of the device.

***Response to Arguments***

Applicant's arguments filed August 6, 2007 have been fully considered but they are not persuasive. Applicants' arguments on pages 6 and 7 are regarding independent claims 1, 8, and 13 which recite the limitation, "a single display connector in communication with the graphics processing unit." Applicants argue "the combination of Lafleur and Trottier fails to teach or suggest the element of "single display connector in communication with the graphics processing unit" as required by independent claims 1, 8, and 13." Applicants also point out the definition in the specification of single display device connectors as "display device connectors that were originally designed to independently control only a single display device."

Lafleur teaches a multi channel coupler, which can control **one or more** display monitors (column 3, lines 44-49). Because the connector has the capability to control just one display monitor, the "multi-channel coupler" can be defined as a "single device connector." Also, the specification of the application (paragraph [0012]) further points out that the "single device connector can include, but is not limited to, a DVI-I connector, a Digital Visual Interface-Digital (DVI-D) connector, a Digital Visual Interface-Analog (DVI-A) connector, a 15-pin Video Graphics Adapter (VGA) connector, a 9-pin Monochrome Display Adapter/Computer Graphics Adapter/Enhanced Graphics Adapter (MDA/CGA/EGA) connector, an Apple Display Connector

(ADC), an Apple Monitor Connector, and a Digital Flat Panel (DFP) connector.” Lafleur expressly teaches the multi-channel coupler as a DVI-I connector (column 3, lines 46-47). This also suggests that the “multi-channel coupler” can be considered a “single device connector.” Trottier is used in combination with Lafleur to show independently controlling multiple monitors to drive separate images through a connector.

### ***Conclusion***

4. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jacinta Crawford whose telephone number is (571) 270-1539. The examiner can normally be reached on M-F 8:00a.m. - 5:00p.m. EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Xiao Wu can be reached on (571) 272-7761. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

JMC 

  
XIAO WU  
SUPERVISORY PATENT EXAMINER